

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 217755US2S		SERIAL NO. NEW APPLICATION	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Tomoaki SHINO			
				FILING DATE HEREWITH		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
<i>MELW</i>	AA	4,809,056	02/28/89	Takehide SHIRATO, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION			
				YES	NO		
<i>MELW</i> ↓	AO	62-76675	04/08/87	JAPAN			X
	AP	05-52672	08/06/93	JAPAN			X
	AQ	61-43475	03/03/86	JAPAN			X
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	Kerry BERNSTEIN, et al., "SOI CIRCUIT DESIGN CONCEPTS", SOI Device Structures, 2000, Chap. 2, Sec. 2.4.2, pp 22-23					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <i>Matthew E. Warner</i>				Date Considered <i>10/2/03</i>			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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